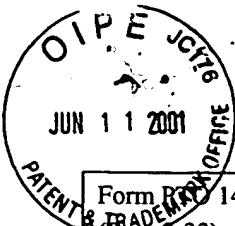




<p>Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office</p> <p style="text-align: right;">ENT & TRADEMARKS 2000 SEARCHED INDEXED SERIALIZED FILED FEB 27 2003</p>					Atty. Docket No. FOV-036		Serial No. 09/515,807	
Information Disclosure Statement by Applicant					Applicant: Merrill, et al.			
(Use several sheets if necessary)					Filed: February 29, 2000 Group:			
U.S. Patent Documents								
Init.		Document No.	Date	Name	Class	Subclass	Filing Date	
<i>WY</i>		5,742,058	4/21/98	Pantigny, et al.	250	370.08	5/30/96	
Foreign Documents								
Translation								
Init.		Document No.	Date	Country	Class	Subclass	Yes	No
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)								
<i>WY</i>	Orly Yadid-Pecht, et al. "A Random Access Photodiode Array for Intelligent Image Capture", IEEE Transactions on Electron Devices, Vol. 38. No. 8. August 1991, pp. 1772-1780							
Examiner					Date Considered <i>10/27/03</i>			
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.								



RECEIVED

JUN 19 2001

Page 1 of 4

Form PTO-1449 (Rev. 2-32)	Technology Center 2600 U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. FOV-036	Serial No. 09/515,807 JUN 15 2001
Information Disclosure Statement by Applicant		Applicant: Merrill et al. TC 2800 MAIL ROOM	
(Use several sheets if necessary)		Filed: Feb. 29, 2000	Group: 2878

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date

Foreign Documents

					Translation			
Init.		Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

1	Abbas El Gamal, et al. " <u>Modeling and Estimation of FPN Components in CMOS Image Sensors</u> ", Information Systems Laboratory, Stanford University, and Fudan University, Shanghai, China, pp. 1-10, (sometime after 1/98).
2	Albert J. P. Theuwissen, " <u>Solid-State Imaging with Charge-Coupled Devices</u> ", Kluwer Academic Publishers, 1997, Chapter 5, pp. 131-141
3	Alex Dickinson, et al., " <u>TP 13.5 : A 256 x 256 CMOS Active Pixel Image Sensor With Motion Detection</u> ", IEEE International Solid-State Circuits Conference, 1995, pp. 226-227.
4	Andrew J. Blanksby, et al. " <u>Noise Performance of a Color CMOS Photogate Image Sensor</u> ", IEEE, IEDM, 1997, pp. 202-204
5	Bob Weibel, " <u>High-End Digital Cameras Can Make Professional Indoor Photography A Snap</u> ", Buyer's Guide, Published April 1997, pp. 71-78.
6	Carver A. Mead, et al. " <u>Scanners For Visualizing Activity of Analog VLSI Circuitry</u> ", California Institute of Technology Computation and Neural Systems Program, July 5, 1991, pp. 2-29.
7	Chye Huat Aw, et al. " <u>A 128 x 128-Pixel Standard-CMOS Image Sensor With Electronic Shutter</u> ", IEEE International Solid-State Circuits Conference, 1996, pp. 4-39 to 4-40.
8	Don Sutherland, " <u>Neaveau Niche-Part I, The Latest in Digital SLRs</u> ", Shutterbug, November 1997, pp. 192, 193, 202, 208 & 210.
9	D. Knipp, et al. " <u>Low Cost Approach to Realize Novel Detectors For Color Recognition</u> ", Forschungszentrum Julich GmbH, ISI-PV,(year unknown), pp. 350-353.
10	Eric R. Fossum, " <u>Active Pixel Sensors: Are CCD's Dinosaurs?</u> ", SPIE Vol. 1900, 7/93, pp. 2-14.

Examiner  Date Considered 1/27/03

Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.



RECEIVED

JUN 19 2001

Page 2 of 4

RECEIVED

Form PTO 1449 (Rev. 2-32)		Technology Center 2600	Atty. Docket No. FOV-036	Serial No. 09/515,807 JUN 15 2001
U.S. Department of Commerce Patent and Trademark Office		Information Disclosure Statement by Applicant		
		Applicant: Merrill et al. TC 2800 MAIL ROOM		
		(Use several sheets if necessary) Filed: Feb. 29, 2000 Group: 2878		
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)				
11	Guang Yang, et al. "A Snap-Shot CMOS Active Pixel Imager For Low-Noise, High-Speed Imaging", IEEE, 1998, pp. 45-48			
12	Hon-Sum Philip Wong, "CMOS Image Sensors-Recent Advances and Device Scaling Considerations", IEDM, 1997 IEE, pp. 201-204.			
13	Hon-Sum Philip Wong, et al. "CMOS Active Pixel Image Sensors Fabricated Using a 1.8-V, 0.25- μ m CMOS Technology", IEEE, April, 1998, pp. 889-894.			
14	Hon-Sum Wong, et al. "Technology and Device Scaling Considerations for CMOS Imagers", IEEE Transactions on Electron Devices, Vol. 43 No. 12, December 1996, pp. 2131-2142.			
15	Ken Parulski, et al. "Enabling Technologies for a Family of Digital Cameras", Eastman Kodak Company, SPIE Vol. 2654, 1996, Invited Paper, pp. 156-163.			
16	Mohamed Ben Chouikha, et al. "Buried Triple p-n Junction Structure in a BiCMOS Technology for Color Detection", IEEE BCTM 6.4, 9/1997 pp. 108-111.			
17	Mohamed Ben Chouikha, et al. "Color Sensitive Photodetectors in Standard CMOS and BiCMOS Technologies", SPIE Vol. 2950, 1996, pp. 108-120.			
18	Orly Yadid-Pecht, et al. "A Random Access Photodiode Array for Intelligent Image Capture", IEEE Transactions on Electron Devices, Vol. 38, No. 8, August 1991, pp. 1772-1780.			
19	R. Daniel McGrath, et al. "FA 11.2: Current-Mediated, Current-Reset 768 x 512 Active Pixel Sensor Array", IEEE Digest of Technical Papers, February 7, 1997, pp 182-183, 138-139			
20	R.M. Guidash, et al. "A 0.6 μ m CMOS Pinned Photodiode Color Imager Technology", IEDM, 1997, pp 927-929.			
21	R. Panicacci, "1/4 Inch CMOS Active Pixel Sensor With Smart On-Chip Functions and Full Digital Interface, Part 1., Brief Introduction to CMOS Image Sensors", Hot Chips IX, August 25-26, 1997, pp. 41-53.			
22	Savvas G. Chamberlain, "Photosensitivity and Scanning of Silicon Image Detector Arrays" IEEE Journal of Solid-State Circuits, Vol. SC-4, No. 6, December 1969, pp. 333-342.			
Examiner			Date Considered <u>10/27/05</u>	
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.				

JUN 11 2001

U.S. Patent and Trademark Office

RECEIVED

JUN 19 2001

Page 3 of 4

RECEIVED

Serial No.
09/515,807
JUN 15 2001

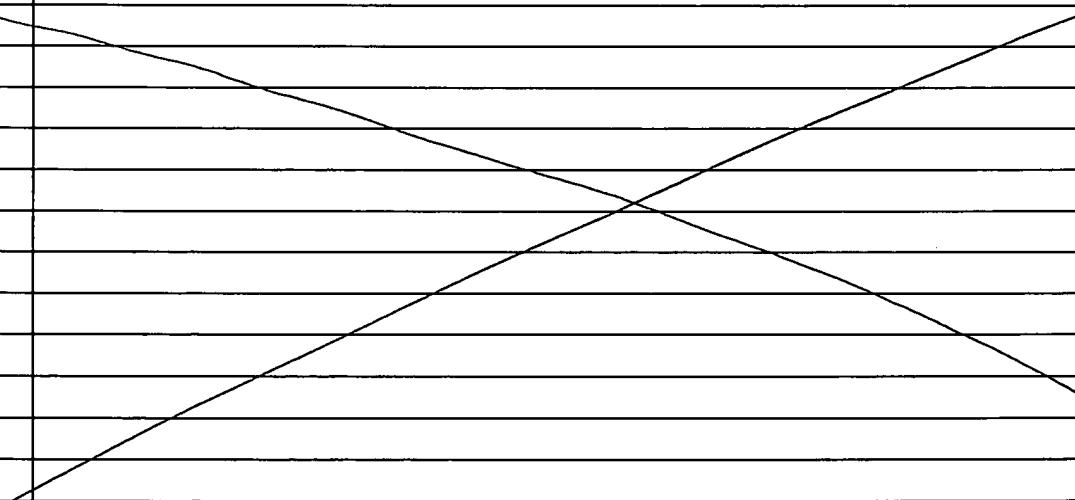
Form PTO 1449 (Rev. 2-32)		Technology Center 2600 U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. FOV-036	TC 2600 MAIL ROOM
Information Disclosure Statement by Applicant		Applicant: Merrill et al.		
(Use several sheets if necessary)		Filed: Feb. 29, 2000	Group: 2878	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)				
W	23	Stephen John Decker, " <u>A Wide Dynamic Range CMOS Imager with Parallel On-Chip Analog-to Digital Conversion</u> ", Submitted to the Department of Electrical Engineering and Computer Science at Massachusetts Institute of Technology, September 1997, pp. 3-205.		
W	24	Sunetra K. Mendis, et al. " <u>A 128 x 128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems</u> ", IEEE, 1993, pp. 583-586.		
W	25	T. Delbruck et al, " <u>Analog VLSI Phototransduction by Continuous-time, Adaptive, Logarithmic Photoreceptor Circuits</u> ", California Institute of Technology Computation and Neural Systems Program, CNS Memo No. 30, April 2, 1996, pp. 24.		
W	26	Tobi Delbruck, et al. " <u>Analog VLSI Adaptive, Logarithmic, Wide-Dynamic-Range Photoreceptor</u> ", IEEE, May 1994, pp. 4 pages.		
W	27	Tatsuo Nomoto, et al. " <u>FA 11.4: A 4M-Pixel CMD Image Sensor With Block and Skip Access Capability</u> ", IEEE International Solid-State Circuits Conference, 1997, 6 pages.		
W	28	Hiroki Miura, et al., " <u>A 100 Frame/s CMOS Active Pixel Sensor For 3D-Gesture Recognition System</u> ", IEEE International Solid-State Circuits Conference, June 1999, pp. 142-143.		
W	29	Yoshiaki Hagiwara, " <u>High-Density and High-Quality Frame Transfer CCD Imager With Very Low Smear, Low Dark Current, and Very High Blue Sensitivity</u> ", IEEE Transactions on Electron Devices, Vol. 43, No. 12, December 1996, pp. 2122-2130.		
W	30	D. Renshaw, et al., " <u>ASIC Image Sensors</u> ", IEEE, 1990, pp. 3038-3041.		
W	31	Peter B. Denyer, et al., " <u>On-Chip CMOS Sensors For VLSI Imaging Systems</u> ", VLSI 91, Proceedings of the IFIP TC 10/WG 10.5 International Conference on Very Large Scale Integration, August 1991, pp. 4b.1.1 to 4b.2.1.		
W	32	J.E.D Hurwitz, et al. " <u>An 800K-Pixel Color CMOS Sensor For Consumer Still Cameras</u> ", SPIE Vol. 3019, 1997, pp. 115-124.		
W	33	Rudolph H. Dyck et al., " <u>Integrated Arrays of Silicon Photodetectors For Image Sensing</u> ", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 196-201.		
W	34	Peter J. W. Noble, " <u>Self-Scanned Silicon Image Detector Arrays</u> ", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 202-208.		
Examiner		Date Considered <u>10/27/03</u>		
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.				



RECEIVED

JUN 19 2001

Page 4 of 4

Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office		Technology Center 2600	Atty. Docket No. FOV-036	Serial No. 09/515,807
Information Disclosure Statement by Applicant		Applicant: Merrill et al./		
(Use several sheets if necessary)		Filed: Feb. 29, 2000 Group: 2878		
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)				
<i>WJ</i>	35	G. Sadasiv, et al., " <u>Thin-Film Circuits For Scanning Image-Sensor Arrays</u> ", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 215-219.		
<i>WJ</i>	36	Thierry M. Bernard, " <u>Advanced Focal Plane Arrays and Electronic Cameras</u> ", Proceedings EurOpt Series, Vol. 2950, October 1996, pp. 111-120.		
<i>WJ</i>	37	S. Decker et al, " <u>A 256 X 256 CMOS Imaging Array With Wide Dynamic Range Pixels and Column-Parallel Digital Output</u> ", IEEE International Solid-State Circuits Conf., 1998, pp. 176-177.		
				
Examiner		Date Considered <i>10/27/03</i>		



Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. FOV-036	Serial No. 09/515,807
---	--	--------------------------	-----------------------

Information Disclosure Statement by Applicant

Applicant: Merrill et al.

(Use several sheets if necessary)

Filed: 02/29/00 Group: 2878

 RECEIVED
JUN 22 2001

Technology Center 2600

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
✓	1	3,866,067	02/11/75	Amelio	307	311	05/21/73
✓	2	3,934,161	01/20/76	Caywood	307	311	04/29/74
✓	3	3,971,065	07/20/76	Bayer	358	41	03/05/75
✓	4	3,988,619	10/26/76	Malaviya	307	311	12/27/74
✓	5	4,011,016	03/08/77	Layne et al.	356	195	09/23/75
✓	6	4,236,760	12/02/80	Haar et al.	303	6 C	03/01/79
✓	7	4,238,760	12/09/80	Carr	357	30	10/06/78
✓	8	4,309,604	01/05/82	Yoshikawa et al.	250	226	07/24/79
✓	9	4,363,963	12/14/82	Ando	250	211	02/26/80
✓	10	4,473,836	09/25/84	Chamberlain	357	30	05/03/82
✓	11	4,499,529	02/12/85	Figueroa	362	283	08/24/83
✓	12	4,499,590	02/12/85	Bluzer	377	60	01/14/83
✓	13	4,573,077	02/25/86	Imai	358	212	09/04/84
✓	14	4,613,895	09/23/86	Burkey et al.	358	41	11/13/78
✓	15	4,626,915	12/02/86	Takatsu	358	213	07/10/84
✓	16	4,651,001	03/17/87	Harada et al.	250	330	12/13/84
✓	17	4,654,714	03/31/87	Hurst, Jr. et al.	358	213	10/30/85
✓	18	4,677,289	06/30/87	Nozaki et al.	250	226	11/04/85
✓	19	4,704,633	11/03/87	Matsumoto	358	213.27	02/20/86
✓	20	4,734,776	03/29/88	Wang et al.	358	213.31	08/15/86
✓	21	4,742,238	05/03/88	Sato	250	578	10/01/86
✓	22	4,786,818	11/22/88	Mead et al.	250	578	11/09/87
✓	23	4,809,075	02/28/89	Akimoto et al.	368	213.18	10/19/87
✓	24	4,839,735	06/13/89	Kyomasu et al.	358	213.31	12/21/87
✓	25	4,845,553	07/04/89	Konomura et al.	358	98	05/19/88

Foreign Documents

Translation

Init.		Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner	Date Considered	10/27/03
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.		



Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. FOV-036	Serial No. 09/515,807
---	--	--	--	-----------------------------	--------------------------

Information Disclosure Statement by Applicant				Applicant: Merrill et al.		
---	--	--	--	---------------------------	--	--

(Use several sheets if necessary)				Filed: 02/29/00	Group: 2878
-----------------------------------	--	--	--	-----------------	-------------

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
W	26	4,875,091	10/17/89	Yamada et al.	358	42	03/16/88
W	27	4,901,129	02/13/90	Hynecek	357	30	03/23/89
W	28	4,942,473	07/17/90	Zeevi et al.	358	213.26	06/30/88
W	29	5,014,107	05/07/91	Vora	357	44	08/29/89
W	30	5,021,853	06/04/91	Mistry	357	23.13	04/27/90
W	31	5,038,214	08/06/91	Miida	358	213.11	03/10/89
W	32	5,055,418	10/08/91	Vora	437	31	11/13/90
W	33	5,117,292	05/26/92	Matsunaga	358	213.19	04/27/90
W	34	5,161,024	11/03/92	Oishi	358	213.24	05/10/91
W	35	5,227,887	07/13/93	Dohi et al.	358	213.27	12/20/91
W	36	5,241,575	08/31/93	Miyatake et al.	377	60	09/09/92
W	37	5,276,521	01/04/94	Mori	358	213.31	12/30/92
W	38	5,289,023	02/22/94	Mead	257	291	08/07/92
W	39	5,317,174	05/31/94	Hynecek	257	222	02/19/93
W	40	5,335,015	08/02/94	Cooper et al.	348	302	10/30/92
W	41	5,341,008	08/23/94	Hynecek	257	231	09/21/93
W	42	5,355,165	10/11/94	Kosonocky et al.	348	311	08/06/92
W	43	5,396,289	03/07/95	Nakamura	348	294	07/06/93
W	44	5,397,734	03/14/95	Iguchi et al.	437	70	10/02/92
W	45	5,414,464	05/09/95	Sasaki	348	222	04/05/94
W	46	5,414,465	05/09/95	Kodama et al.	348	236	01/03/94
W	47	5,414,683	05/09/95	Tani	36.9	47	10/28/92
W	48	5,424,223	06/13/95	Hynecek	437	3	06/06/94
W	49	5,428,390	06/27/95	Cooper et al.	348	240	01/21/94
W	50	5,434,620	07/18/95	Higuchi et al.	348	308	09/28/93
W	51	5,461,425	10/24/95	Fowler et al.	348	294	02/15/94
W	52	5,471,245	11/28/95	Cooper et al.	348	302	05/19/94
W	53	5,502,299	03/26/96	Standley	250	208.2	12/12/94
W	54	5,541,402	07/30/96	Ackland et al.	250	208.1	10/17/94
W	55	5,541,654	07/30/96	Roberts	348	301	06/08/95
W	56	5,547,881	08/20/96	Wang et al.	437	24	03/06/96

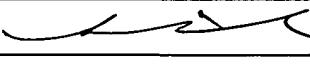
Examiner *Rie*

Date Considered *10/27/03*

Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.

RECEIVED
JUN 22 2001
Technology Center 2600



Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office					Atty. Docket No. FOV-036	Serial No. 09/29/00	
Information Disclosure Statement by Applicant					Applicant: Merrill et al.		
(Use several sheets if necessary)					Filed: 02/29/00	Group: 2878	
U.S. Patent Documents							
Init.		Document No.	Date	Name	Class	Subclass	Filing Date
<input checked="" type="checkbox"/>	57	5,572,074	11/05/96	Standley	307	117	06/06/95
<input checked="" type="checkbox"/>	58	5,576,763	11/19/96	Ackland et al.	348	303	11/22/94
<input checked="" type="checkbox"/>	59	5,589,423	12/31/96	White et al.	437	228	10/03/94
<input checked="" type="checkbox"/>	60	5,625,210	04/29/97	Lee et al.	257	292	04/13/95
<input checked="" type="checkbox"/>	61	5,631,704	05/20/97	Dickinson et al.	348	308	10/14/94
<input checked="" type="checkbox"/>	62	5,668,596	09/16/97	Vogel	348	222	02/29/96
<input checked="" type="checkbox"/>	63	5,705,441	01/06/98	Wang et al.	438	384	03/19/96
<input checked="" type="checkbox"/>	64	5,712,682	01/27/98	Hannah	348	255	12/02/96
<input checked="" type="checkbox"/>	65	5,739,562	04/14/98	Ackland et al.	257	291	08/01/95
<input checked="" type="checkbox"/>	66	5,742,058	04/21/98	Pantigny	250	370.08	05/30/96
<input checked="" type="checkbox"/>	67	5,801,657	09/01/98	Fowler et al.	341	155	02/05/97
<input checked="" type="checkbox"/>	68	5,883,421	03/16/99	Ben Chouikha et al.	257	461	03/13/97
<input checked="" type="checkbox"/>	69	6,078,037	06/20/00	Booth, Jr.	250	208.1	04/16/98
<input checked="" type="checkbox"/>	70	6,115,066	09/05/00	Gowda et al.	348	308	06/12/97
<input checked="" type="checkbox"/>	71	6,211,510 B1	04/03/01	Merrill et al.	250	208.1	01/26/00
Examiner 					Date Considered <u>10/27/01</u>		
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.							

RECEIVED
JUN 22 2001
Technology Center 2600



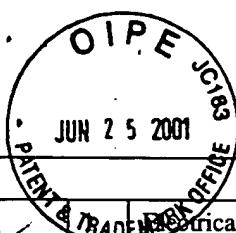
RECEIVED

JUL 10 2001

Technology Center 2600

Page 1 of 2

Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office					Atty. Docket No. FOV-036	Serial No. 09/515,807	
Information Disclosure Statement by Applicant					Applicant: Merrill, et al.		
(Use several sheets if necessary)					Filed: February 29, 2000 Group: 2878		
U.S. Patent Documents							
Init.		Document No.	Date	Name	Class	Subclass	Filing Date
W	1	6,046,444	4/4/00	Morteza Afghahi	250	208.1	12/8/97
W	2	6,246,436	6/12/01	Lin, et al.	348	308	11/3/97
Foreign Documents							
Translation							
Init.		Document No.	Date	Country	Class	Subclass	Yes No
W	1	0 466 929 A1	22.01.92	European	H04N 5	238	
W	2	0 605 898 A1	13.07.94	European	H01L 27	146	
W	3	EP 0707416 A2	17.04.96	European	H04N 3	15	
W	4	EP 0 777 379 A1	04.06.97	European	H04N 3	15	
W	5	EP 0 854 516 A2	22.07.98	European	H01L 27	146	
W	6	62143571	26.06.87	European	H04N 5	335	
W	7	01103378	20.04.89	European	H04N 5	335	
W	8	06133320	13.05.94	European	H04N 9	09	
W	9	08009391	12.01.96	European	H04N 9	04	
W	10	DE 43 04 506 A1	18.8.94	Germany	H 04 N 5	225	X
W	11	DE 198 36 356 A1	12.5.99	Germany	H 01 L 27	146	X
W	12	08095670	12.04.96	European	G06F 1	16	
W	13	54-108628	8.25.79	Japan	G03B 7	20	X
W	14	09128091	16.05.97	European	G06F 1	00	
W	15	58-83824	5.19.83	Japan	G 03 B 7	20	X
W	16	59-152424	8.31.84	Japan	G 03 B 7	20	X
W	17	59-42527	3.9.84	Japan	G 03 B 17	14	X
W	18	60-23841	2.6.85	Japan	G 03 B 17	14	X
W	19	60-53912	3.28.85	Japan	G 02 B 7	11	X
W	20	WO 96/15626	23.05.96	Germany	H04N 5	335	
W	21	WO 97/17800	15.05.97	Germany	H04N 3	143	
W	22	WO 98/19455	07.05.98	Germany	H04N 3	15	
W	23	WO 00/72604 A1	30.11.00	Germany	H04N 9	04	
W	24	WO 99/66560	23.12.99	Germany	H01L 27	144	
W	25	WO 99/66712	23.12.99	Germany	H04N 3	15	



Page 2 of 2

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)		
iy	1	Electrically Erasable and Reprogrammable Read-Only Memory Using the n-Channel SIMOS One-Transistor Cell; IEEE Transactions on Electron Devices, Vol. ED-24, No. 5, May 1977, Bernward Rossler
iy	2	Physics of Semiconductor Devices; Wiley-Interscience; Murray Hill, New Jersey; Bell Telephone Laboratories, Inc., S.M. SZE, 1969
Examiner		Date Considered <u>10/27/03</u>
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.		

RECEIVED
JUL 10 2001
Technology Center 2600

RECEIVED
JUL 5 2001
TC 2600 MAIL ROOM



Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.